



## UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
United States Patent and Trademark Office  
Address: COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, Virginia 22313-1450  
www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 6111

SERIAL NUMBER 10/701,249	FILING DATE 11/04/2003  RULE	CLASS 716	GROUP ART UNIT 2825	ATTORNEY DOCKET NO. 2271/71388
-----------------------------	---------------------------------------	--------------	------------------------	--------------------------------------

## APPLICANTS

Junji Ichimiya, Osaka, JAPAN;

\*\* CONTINUING DATA \*\*\*\*\*

NONE TT

\*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2002-320912 11/05/2002

VERIFIED TT

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 02/03/2004

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged	<u>Junji Ichimiya</u> Examiner's Signature Initials TT	STATE OR COUNTRY JAPAN	SHEETS DRAWING 18	TOTAL CLAIMS 12	INDEPENDENT CLAIMS 2
---	--	------------------------------	-------------------------	-----------------------	----------------------------

## ADDRESS

Ivan S. Kavrukov, Esq.  
Cooper & Dunham LLP  
1185 Avenue of the Americas  
New York, NY  
10036

## TITLE

Layout design method for semiconductor integrated circuit, and semiconductor integrated circuit

FILING FEE  RECEIVED 900	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
-----------------------------------	---	--